
Installation and testing

Installation and testing procedures list the steps required to install and maintain the Enhanced Asynchronous Interface Line Unit (EAILU).

Preinstallation information

Perform the following tasks before installing the EAILU:

- Install all DACs and I/O cables.

Note: Refer to *NT7D16 Data Access Card description and operation* (553-3001-191) for detailed information on available intelligent peripheral equipment (IPE) card slot positions for DAC installation and I/O panel cable terminations to the Main Distribution Frame (MDF).

- Install all AILCs and I/O cables.

Note: An AILC can be installed in slots 1 through 10 of a peripheral equipment (PE) module/shelf but cannot be installed in an IPE module. Refer to **Tables 3** and **4** for AILC and I/O cable wire and pin assignment information.

- Install all RJ-11 jacks, distribution blocks, facility wiring, and cross-connect wiring as illustrated in **Figure 5**. Ensure that the line length does not exceed 4000 ft using 26-gauge wire.

Note: The DTE that the EAILU is connected to must not share the same power outlet strip with electromagnetic interference (EMI) noisy equipment such as a fluorescent lamp. Otherwise, data corruption may occur.

- Install the DTE device.
- Configure Meridian 1 using the LD11 program to identify all DAC and AILC ports supporting EAILUs.

Note: If adding or changing AILC ports that support EAILUs, refer to *Meridian Data Services description* (553-2731-100). If adding or changing DAC ports, refer to *NT7D16 Data Access Card description and operation* (553-3001-191). Make sure all DAC ports supporting EAILUs are set to RS-422 using the LD 11 program by setting TYPE = RS-422. The minimum voltage levels, when the EAILU is connected to the Asynchronous Interface Line Card (AILC), are listed here.

- 700 mV between SDB and SDA at the EAILU
- 500 mV between SDB and SDA at the MDF
- 700 mV between RDB and RDA at the EAILU

EAILU installation

To install the EAILU into an asynchronous data link between Meridian 1 and a terminal or a host computer, follow the steps below.

- 1 Unpack and inspect the EAILU. Tag and return any defective units according to local instructions.
- 2 Connect the DB-25 connector on the EAILU to the appropriate DB-25 RS-232 connector at the rear of the DTE device. See [Figure 5](#).
- 3 Secure the EAILU connector to the device connector with the attached screws.
- 4 Insert the RJ-11 plug of the EAILU into the RJ-11 wall-mounted telephone jack. Ensure that the clip on the RJ-11 plug snaps securely into place.
- 5 Use EAILU data station keyboard dialing procedures to verify the installation. Refer to *Meridian data features operation and tests* (553-2731-300) for the AIM keyboard dialing procedures.

Table 3

Pair-terminations at the NT8D13 PE Module I/O Panel connectors A, C, E, and G for the AILC QPC430 (Single Loop Mode) (Part 1 of 4)

Port Pairs	AILC Port Pins	Connector Pin Number and Wire Color Code		I/O Panel Connectors				AILC Card
				A	C	E	G	
1T 1R 2T 2R 3T 3R 4T 4R	RDA0 RDB0 SDA0 SDB0	26 1 27 2 28 3 29 4	W-BL BL-W W-O O-W W-G G-W W-BR BR-W	Slot X-1 Card 1	Slot X-4 Card 4	Slot X-7 Card 7	Slot X-10 Card 10	Unit 0
5T 5R 6T 6R 7T 7R 8T 8R	RDA1 DRB1 SDA1 SDB1	30 5 31 6 32 7 33 8	W-S S-W R-BL BL-R R-O O-R R-G G-R					Unit 1
9T 9R 10T 10R 11T 11R 12T 12R	RDA2 RDB2 SDA2 SDB2	34 9 35 10 36 11 37 12	R-BR BR-R R-S S-R BK-BL BL-BK BL-O O-BL					Unit 2
13T 13R 14T 14R 15T 15R 16T 16R	RDA3 RDB3 SDA3 SDB3	38 13 39 14 40 15 41 16	BK-G G-BK BK-BR BR-BK BK-S S-BK Y-BL BL-Y					Unit 3

Table 3
Pair-terminations at the NT8D13 PE Module I/O Panel connectors A, C, E, and G for the AILC QPC430 (Single Loop Mode) (Part 2 of 4)

Port Pairs	AILC Port Pins	Connector Pin Number and Wire Color Code		I/O Panel Connectors				AILC Card
				A	C	E	G	
17T 17R 18T 18R 19T 19R 20T 20R	RDA0 RDB0 SDA0 SDB0	42 17 43 18 44 19 45 20	Y-O O-Y Y-G G-Y Y-BR BR-Y Y-S S-Y	Slot X-2 Card 2	Slot X-5 Card 5	Slot X-8 Card 8	Spare	Unit 0
21T 21R 22T 22R 23T 23R 24T 24R	RDA1 RDB1 SDA1 SDB1	46 21 47 22 48 23 49 24	V-BL BL-V V-O O-V V-BR BR-V V-S S-V					Unit 1

Table 3

Pair-terminations at the NT8D13 PE Module I/O Panel connectors A, C, E, and G for the AILC QPC430 (Single Loop Mode) (Part 3 of 4)

Port Pairs	AILC Port Pins	Connector Pin Number and Wire Color Code		I/O Panel Connectors				AILC Card
				A	C	E	G	
1T 1R 2T 2R 3T 3R 4T 4R	RDA0 RDB0 SDA0 SDB0	26 1 27 2 28 3 29 4	W-BL BL-W W-O O-W W-G G-W W-BR BR-W	Slot X-1 Card 1	Slot X-4 Card 4	Slot Y-2 Card 2	Slot Y-5 Card 5	Unit 0
5T 5R 6T 6R 7T 7R 8T 8R	RDA1 RDB1 SDA1 SDB1	30 5 31 6 32 7 33 8	W-S S-W R-BL BL-R R-O O-R R-G G-R					Unit 1
9T 9R 10T 10R 11T 11R 12T 12R	RDA2 RDB2 SDA2 SDB2	34 9 35 10 36 11 37 12	R-BR BR-R R-S S-R BK-BL BL-BK BL-O O-BL					Unit 2
13T 13R 14T 14R 15T 15R 16T 16R	RDA3 RDB3 SDA3 SDB3	38 13 39 14 40 15 41 16	BK-G G-BK BK-BR BR-BK BK-S S-BK Y-BL BL-Y					Unit 3

Table 3
Pair-terminations at the NT8D13 PE Module I/O Panel connectors A, C, E, and G for the AILC QPC430 (Single Loop Mode) (Part 4 of 4)

Port Pairs	AILC Port Pins	Connector Pin Number and Wire Color Code		I/O Panel Connectors				AILC Card
				A	C	E	G	
17T 17R 18T 18R 19T 19R 20T 20R	RDA0 RDB0 SDA0 SDB0	42 17 43 18 44 19 45 20	Y-O O-Y Y-G G-Y Y-BR BR-Y Y-S S-Y	Slot X-2 Card 2	Slot X-5 Card 5	Slot Y-3 Card 3	Spare	Unit 0
21T 21R 22T 22R 23T 23R 24T 24R	RDA1 RDB1 SDA1 SDB1	46 21 47 22 48 23 49 24	V-BL BL-V V-O O-V V-BR BR-V V-S S-V					Unit 1

Table 4

Pair-terminations at the NT8D13 PE Module I/O Panel connectors B, D, and F for the AILC QPC430 (Single Loop Mode) (Part 1 of 4)

Port Pairs	AILC Port Pins	Connector Pin Number and Wire Color Code		I/O Panel Connectors			AILC Card
				B	D	F	
1T 1R 2T 2R 3T 3R 4T 4R	RDA2 RDB2 SDA2 SDB2	26 1 27 2 28 3 29 4	W-BL BL-W W-O O-W W-G G-W W-BR BR-W	Slot X-2 Card 2	Slot X-5 Card 5	Slot X-8 Card 8	Unit 2
5T 5R 6T 6R 7T 7R 8T 8R	RDA3 RDB3 SDA3 SDB3	30 5 31 6 32 7 33 8	W-S S-W R-BL BL-R R-O O-R R-G G-R				Unit 3

Table 4
Pair-terminations at the NT8D13 PE Module I/O Panel connectors B, D, and F for the AILC QPC430 (Single Loop Mode) (Part 2 of 4)

Port Pairs	AILC Port Pins	Connector Pin Number and Wire Color Code		I/O Panel Connectors			AILC Card
				B	D	F	
9T 9R 10T 10R 11T 11R 12T 12R	RDA0 RDB0 SDA0 SDB0	34 9 35 10 36 11 37 12	R-BR BR-R R-S S-R BK-BL BL-BK BL-O O-BL	Slot X-3 Card 3	Slot X-6 Card 6	Slot X-9 Card 9	Unit 0
13T 13R 14T 14R 15T 15R 16T 16R	RDA1 RDB1 SDA1 SDB1	38 13 39 14 40 15 41 16	BK-G G-BK BK-BR BR-BK BK-S S-BK Y-BL BL-Y				Unit 1
17T 17R 18T 18R 19T 19R 20T 20R	RDA2 RDB2 SDA2 SDB2	42 17 43 18 44 19 45 20	Y-O O-Y Y-G G-Y Y-BR BR-Y Y-S S-Y				Unit 2
21T 21R 22T 22R 23T 23R 24T 24R	RDA3 RDB3 SDA3 SDB3	46 21 47 22 48 23 49 24	V-BL BL-V V-O O-V V-BR BR-V V-S S-V				Unit 3

Table 4

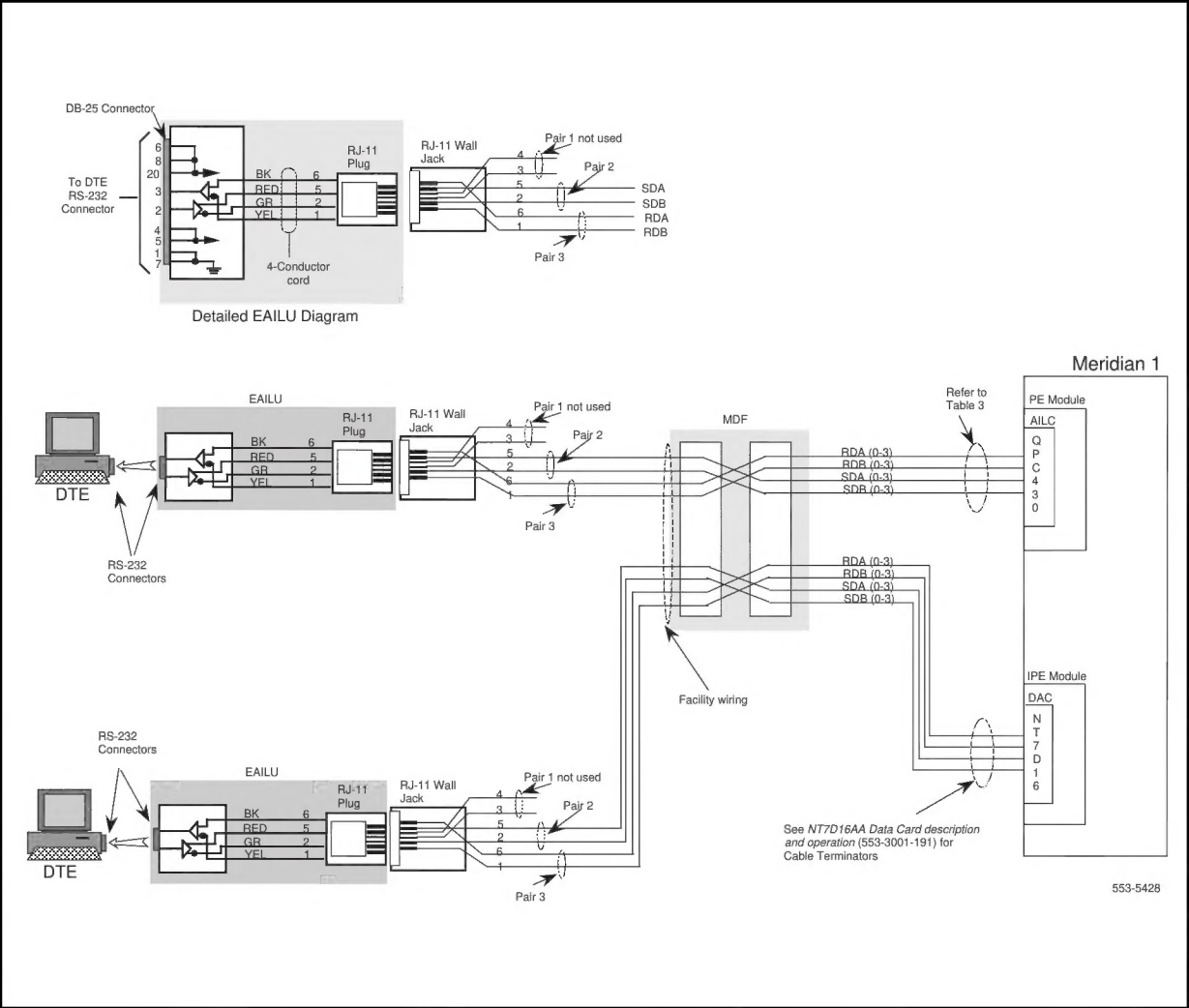
Pair-terminations at the NT8D13 PE Module I/O Panel connectors B, D, and F for the AILC QPC430 (Single Loop Mode) (Part 3 of 4)

Port Pairs	AILC Port Pins	Connector Pin Number and Wire Color Code		I/O Panel Connectors			AILC Card
				B	D	F	
1T 1R 2T 2R 3T 3R 4T 4R	RDA2 RDB2 SDA2 SDB2	26 1 27 2 28 3 29 4	W-BL BL-W W-O O-W W-G G-W W-BR BR-W	Slot X-2 Card 2	Slot X-5 Card 5	Slot Y-3 Card 3	Unit 2
5T 5R 6T 6R 7T 7R 8T 8R	RDA3 RDB3 SDA3 SDB3	30 5 31 6 32 7 33 8	W-S S-W R-BL BL-R R-O O-R R-G G-R				Unit 3

Table 4
Pair-terminations at the NT8D13 PE Module I/O Panel connectors B, D, and F for the AILC QPC430 (Single Loop Mode) (Part 4 of 4)

Port Pairs	AILC Port Pins	Connector Pin Number and Wire Color Code		I/O Panel Connectors			AILC Card
				B	D	F	
9T 9R 10T 10R 11T 11R 12T 12R	RDA0 RDB0 SDA0 SDB0	34 9 35 10 36 11 37 12	R-BR BR-R R-S S-R BK-BL BL-BK BL-O O-BL	Slot X-3 Card 3	Slot Y-1 Card 1	Slot X-4 Card 4	Unit 0
13T 13R 14T 14R 15T 15R 16T 16R	RDA1 RDB1 SDA1 SDB1	38 13 39 14 40 15 41 16	BK-G G-BK BK-BR BR-BK BK-S S-BK Y-BL BL-Y				Unit 1
17T 17R 18T 18R 19T 19R 20T 20R	RDA2 RDB2 SDA2 SDB2	42 17 43 18 44 19 45 20	Y-O O-Y Y-G G-Y Y-BR BR-Y Y-S S-Y				Unit 2
21T 21R 22T 22R 23T 23R 24T 24R	RDA3 RDB3 SDA3 SDB3	46 21 47 22 48 23 49 24	V-BL BL-V V-O O-V V-BR BR-V V-S S-V				Unit 3

Figure 5
EALU connections



Meridian 1

Enhanced Asynchronous Interface Line Unit

Description and formats

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